Computer Arch

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**Milestone 1**

**1-Bit Adder**

**Diagram, schematic

Description automatically generated**

**1-Bit ALU**

**Diagram, schematic

Description automatically generated**

**4-Bit ALU**

**Diagram, schematic

Description automatically generated**

**32-Bit ALU**

-Each box is a 4-bit ALU. Logisim did not allow me to put the label names on them.

**Diagram, schematic

Description automatically generated**

**ALU\_Ctrl**

**Table

Description automatically generated**

**Diagram, schematic

Description automatically generated**

**ImmGen:**

**EXTRA: I decided to finish the ImmGenerator. Professor Moore said to finished it earlier, so I decided to complete that circuit.**

**Diagram, schematic

Description automatically generated**